

Fig. 1

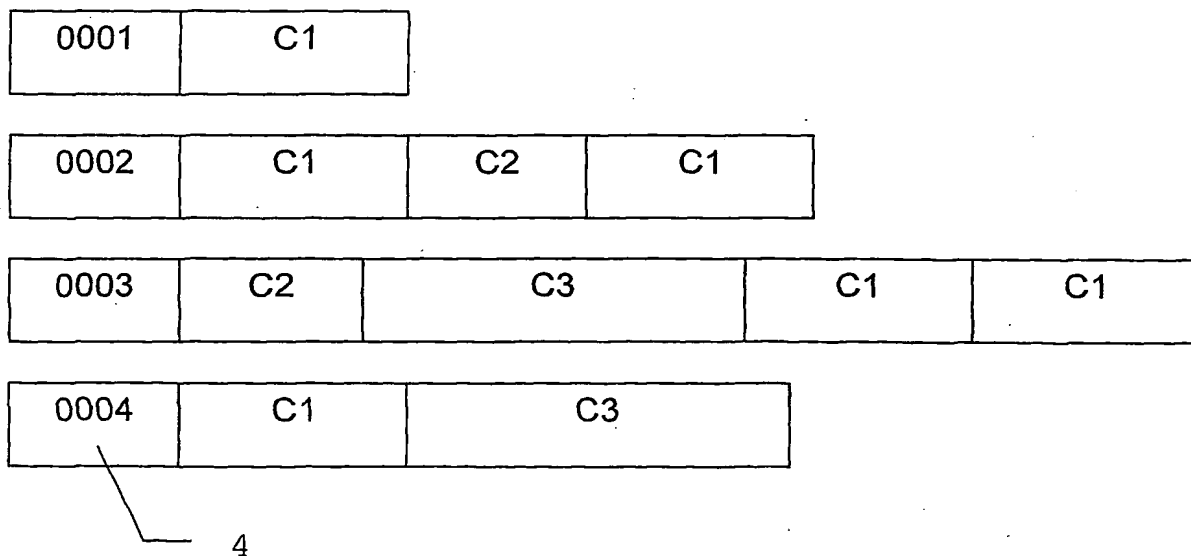


Fig. 2

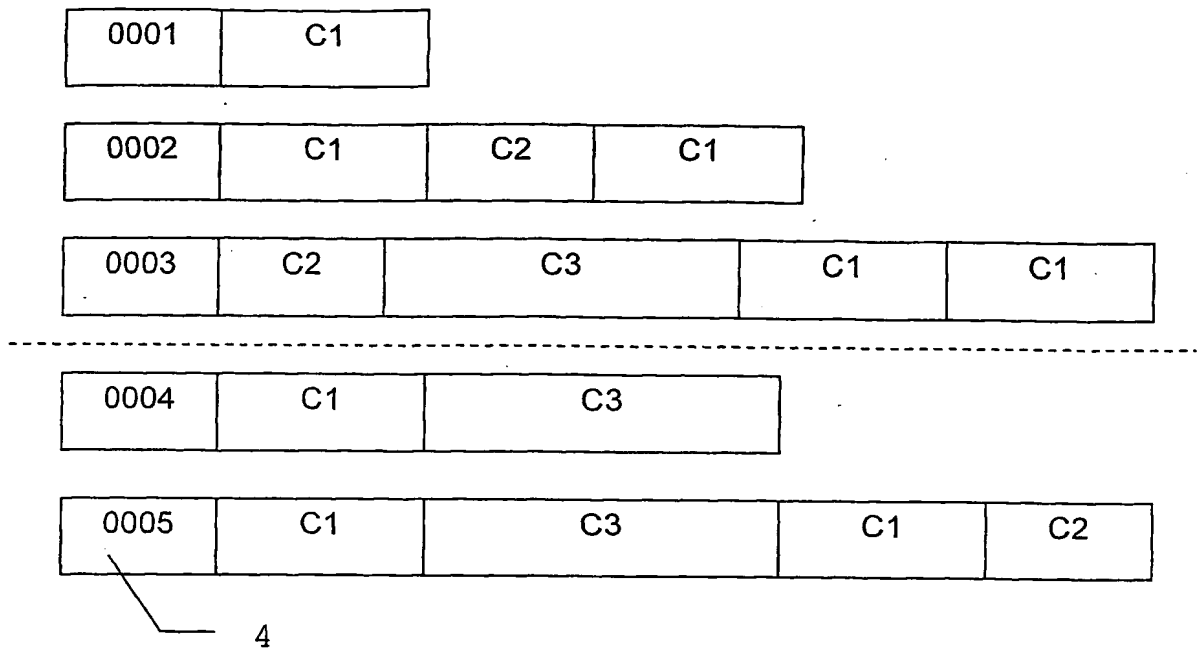


Fig. 3

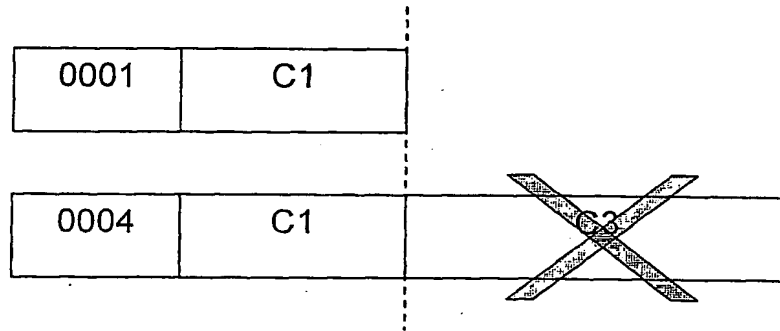


Fig. 4

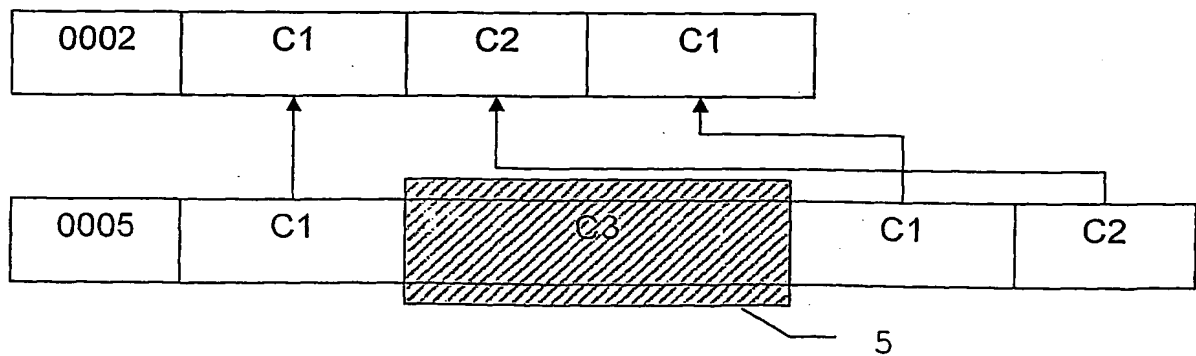


Fig. 5

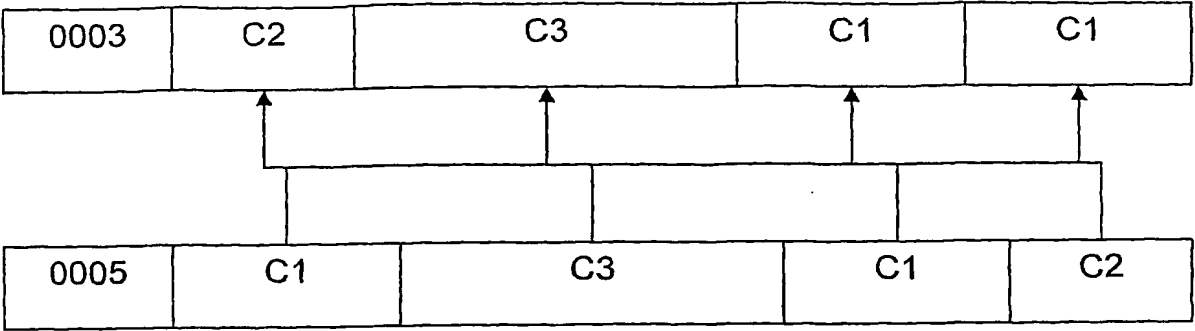


Fig. 6

| | | | | | | | | |
|------|-----|-----|-----|-----|-----|-----|-----|-----|
| 0001 | 01 | 00 | 00 | 00 | 00 | 00 | 00 | 00 |
| 0002 | 01 | 02 | 01 | 00 | 00 | 00 | 00 | 00 |
| 0003 | 02 | 03 | 01 | 01 | 00 | 00 | 00 | 00 |
| 0004 | 01 | 03 | 00 | 00 | 00 | 00 | 00 | 00 |
| 0005 | 01 | 03 | 01 | 02 | 00 | 00 | 00 | 00 |
| ... | ... | ... | ... | ... | ... | ... | ... | ... |

Fig. 7

| | | | | |
|------|----|----|----|----|
| 0001 | C1 | | | |
| 0002 | C1 | C2 | C1 | |
| 0003 | C2 | C3 | C1 | C1 |
| 0004 | C1 | C3 | | |
| 0005 | C1 | C3 | C1 | C2 |
| 0006 | C1 | C2 | C1 | C1 |

Fig. 8

| | | | | | | | | |
|------|------|------|-----|-----|-----|-----|-----|-----|
| 0005 | 0004 | 0002 | 00 | 00 | 00 | 00 | 00 | 01 |
| 0006 | 0002 | 0001 | 00 | 00 | 00 | 00 | 00 | 00 |
| ... | ... | ... | ... | ... | ... | ... | ... | ... |

Fig. 9

| | | | | | |
|---|------|----|----|----|----|
| A | 0007 | C1 | C2 | C4 | C1 |
| B | 0007 | C1 | C2 | C4 | C1 |

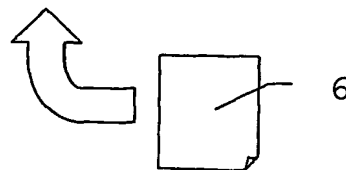


Fig. 10

| | | | | |
|--------|------|--------|-------|-----|
| BIT | 0001 | 00000 | 00001 | 00 |
| UINT16 | 0016 | 00000 | 65535 | 00 |
| INT16 | 0016 | -32768 | 32767 | 00 |
| INT17 | 0017 | -65536 | 65535 | 00 |
| ... | ... | ... | ... | ... |

Fig. 11

| | | | | |
|-----|--------|-----|----------|----------|
| 01 | BIT | 0 | Code=0 | Code=1 |
| 02 | UINT10 | 1 | ANT=0001 | ANT=1023 |
| ... | ... | ... | ... | ... |

Fig. 12

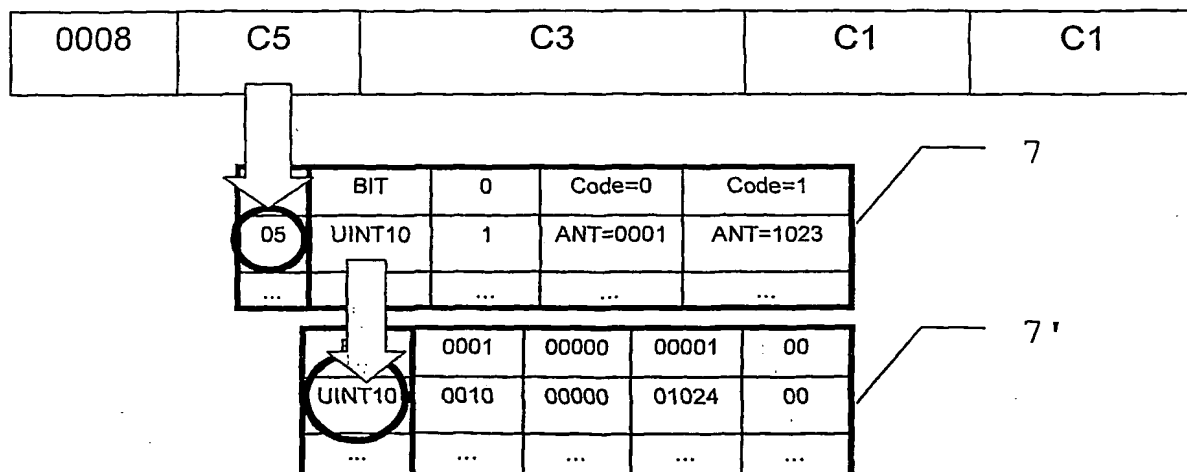


Fig. 13

| | | | | |
|-----|-----|---------|----------|-------|
| 67 | 28 | 0 | 2400,000 | 0,002 |
| 68 | 29 | -12,000 | 1200,000 | 0,001 |
| ... | ... | ... | ... | ... |

Fig. 14

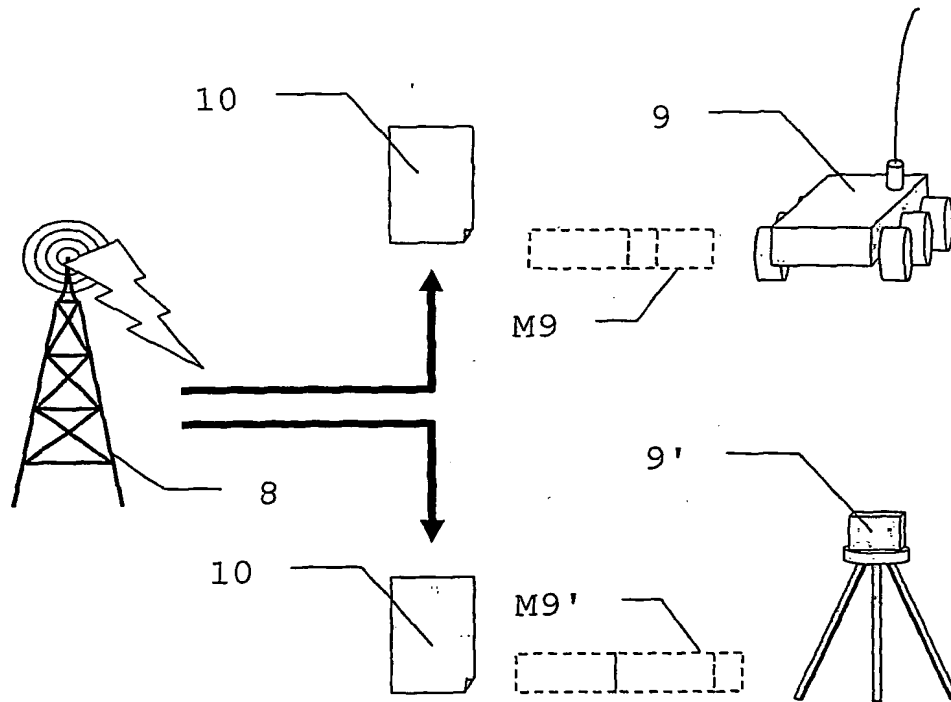


Fig. 15

